

LISTING OF CLAIMS

This Listing of Claims replaces all prior versions and listings of claims in this application.

1. (Canceled)

2. (Currently amended) ~~The device according to claim 1, wherein~~ A device for predistorting an input signal at an amplifier means, comprising a storage means, an offset adding means, and a controller, wherein

the storage means is adapted to store phase values;

the offset adding means is a phase offset adding means for phase-shifting the input signal,

the storage means and the phase offset adding means are connected to the controller, which is adapted to retrieve at least one phase value from the storage means, and output an offset signal comprising the retrieved phase value to the phase offset adding means, which is adapted to add said offset signal to the input signal, and
the storage means comprises a lookup table comprising different gain levels and associated phase values, and the controller is adapted to retrieve a phase value from the storage means corresponding to a given gain level of the amplifier means.

3. (Currently amended) ~~The device according to claim 1, wherein~~ A device for predistorting an input signal at an amplifier means, comprising a storage means, an offset adding means, and a controller, wherein

the storage means is adapted to store phase values;

the offset adding means is a phase offset adding means for phase-shifting the input signal,

the storage means and the phase offset adding means are connected to the controller, which is adapted to retrieve at least one phase value from the storage means, and output an offset signal comprising the retrieved phase value to the phase offset adding means, which is adapted to add said offset signal to the input signal, and
the storage means is a memory comprising a look-up table comprising phase values relating to the change of the gain level of an amplifier means from a first gain level to one or more additional gain levels.

4. (Original) The device according to claim 3, wherein the memory is rewritable.

5. (Currently Amended) The device according to claim ~~[[1]]~~ 2, wherein the phase offset adding means is a complex multiplier.

6. (Currently amended) ~~The device according to claim 1,~~ wherein A device for predistorting an input signal at an amplifier means, comprising a storage means, an offset adding means, and a controller, wherein

the storage means is adapted to store phase values;

the offset adding means is a phase offset adding means for phase-shifting the input signal.

the storage means and the phase offset adding means are connected to the controller, which is adapted to retrieve at least one phase value from the storage means, and output an offset signal comprising the retrieved phase value to the phase offset adding means, which is adapted to add said offset signal to the input signal, and each phase value stored in the storage means corresponds to a change in phase of the output power when the gain level of the amplifier means is changed from a first gain level to a second gain level.

7. (Previously presented) The device according to claim 6, wherein the first gain level is the lowest gain level, and the second gain level is any other gain level of the amplifier means.

8. (Currently amended) ~~The device according to claim 1,~~ wherein A device for predistorting an input signal at an amplifier means, comprising a storage means, an offset adding means, and a controller, wherein

the storage means is adapted to store phase values;

the offset adding means is a phase offset adding means for phase-shifting the input signal.

the storage means and the phase offset adding means are connected to the controller, which is adapted to retrieve at least one phase value from the storage means, and output an offset signal comprising the retrieved phase value to the phase offset adding means, which is adapted to add said offset signal to the input signal, and

the device further comprises a temperature sensing means connected to the controller for deriving a temperature or a temperature interval, and the storage means

comprises a lookup table comprising gain levels and associated phase values for different temperatures or different temperature intervals, and the controller is adapted to retrieve a temperature dependent phase value from the storage means corresponding to a temperature or temperature interval and a given gain level of the amplifier means.

9. (Currently amended) The device according to claim 1, wherein A device for predistorting an input signal at an amplifier means, comprising a storage means, an offset adding means, and a controller, wherein

the storage means is adapted to store phase values;

the offset adding means is a phase offset adding means for phase-shifting the input signal,

the storage means and the phase offset adding means are connected to the controller, which is adapted to retrieve at least one phase value from the storage means, and output an offset signal comprising the retrieved phase value to the phase offset adding means, which is adapted to add said offset signal to the input signal, and

the device further comprises a frequency indicator for deriving an operating frequency value or an operating frequency interval value, and the storage means comprises a lookup table comprising gain levels and associated phase values for different frequencies or different frequency intervals, and the controller is adapted to retrieve a frequency dependent phase value from the storage means corresponding to a frequency or frequency interval and a given gain level of the amplifier means.

10. (Canceled)

11. (Currently amended) The method according to claim 10; A method for predistorting an input signal at an amplifier means including a storage means, comprising the steps of:

retrieving a phase value from a lookup table of the storage means in response to changing a gain level of the amplifier means; and

adding an offset signal having a phase value corresponding to the retrieved phase value to said input signal;

wherein the step of retrieving further comprises the steps of:

receiving a new gain level of the amplifier means; and

retrieving a phase value associated with the new gain level from the look-up table of the storage means.

12. (Currently amended) ~~The method according to claim 10; A method for predistorting an input signal at an amplifier means including a storage means, comprising the steps of:~~

retrieving a phase value from a lookup table of the storage means in response to changing a gain level of the amplifier means; and

adding an offset signal having a phase value corresponding to the retrieved phase value to said input signal;

wherein the phase value corresponding to a basic gain level is zero.

13. (Previously presented) The method according to claim 11, wherein the step of retrieving further comprises the steps of:

obtaining a temperature value or a temperature interval value; and

retrieving a temperature dependent phase value associated with the new gain level and the temperature or temperature interval from the lookup-table of the storage means.

14. (Previously presented) The method according to claim 11, wherein the method comprises the further steps of:

obtaining a frequency value or a frequency interval value; and

retrieving a frequency dependent phase value associated with the new gain level and the frequency or frequency interval from the look-up table of the storage means.

15. (Canceled)

16. (Currently amended) The apparatus device according to claim ~~[[15]]~~ 2, wherein said apparatus the device is a mobile terminal, a pager, or a communicator.

17. (Currently amended) The apparatus device according to claim 15, wherein the apparatus device is a mobile telephone.

18. (New) The device according to claim 3, wherein the phase offset adding means is a complex multiplier.

19. (New) The device according to claim 6, wherein the phase offset adding means is a complex multiplier.

20. (New) The device according to claim 8, wherein the phase offset adding means is a complex multiplier.

21. (New) The device according to claim 9, wherein the phase offset adding means is a complex multiplier.